Flyback Transformers
For STMicroelectronics
POTS interface

• Developed for the STMicro STLC3075 & STLC3085
  Integrated POTS interfaces for home access gateway and WLL.
• Provides negative voltage (~46 V typical) to operate the microchip

Core material Ferrite
Interwinding isolation 1500 Vdc
Terminations RoHS tin-silver over tin over nickel over phos bronze.
Other terminations available at additional cost.
Weight 1.9 – 2.1 g
Ambient temperature –40°C to +85°C
Storage temperature Component: –40°C to +85°C.
Tape and reel packaging: –40°C to +80°C
Resistance to soldering heat Max three 40 second reflows at +260°C, parts cooled to room temperature between cycles
Moisture Sensitivity Level (MSL) 1 (unlimited floor life at <30°C / 85% relative humidity)
Failures in Time (FIT) / Mean Time Between Failures (MTBF)
38 per billion hours / 26,315,789 hours, calculated per Telcordia SR-332
Packaging 450 per 13″ reel Plastic tape: 24 mm wide, 0.45 mm thick, 16 mm pocket spacing, 9.04 mm pocket depth
PCB washing Tested with pure water or alcohol only. For other solvents, see Doc787_PCB_Washing.pdf

<table>
<thead>
<tr>
<th>Part number</th>
<th>Inductance at 0 A² ±8% (µH)</th>
<th>Inductance at Ipk³ min (µH)</th>
<th>DCR max (Ohms)</th>
<th>Leakage inductance⁴ max (µH)</th>
<th>Turns ratio</th>
<th>Ipk³ (A)</th>
<th>Input voltage (V)</th>
</tr>
</thead>
<tbody>
<tr>
<td>FA2469-AL</td>
<td>19</td>
<td>14.86</td>
<td>0.036</td>
<td>16.5</td>
<td>1 : 16</td>
<td>1.5</td>
<td>4.5 – 8.5</td>
</tr>
<tr>
<td>FA2470-AL</td>
<td>19</td>
<td>14.86</td>
<td>0.036</td>
<td>7.92</td>
<td>1 : 8</td>
<td>1.5</td>
<td>8.5 – 12.0</td>
</tr>
</tbody>
</table>

1. When ordering, please specify packaging code:
   FA2470-ALD
   Packaging: D = 13″ machine-ready reel. EIA-481 embossed plastic tape (350 parts per full reel).
   B = Less than full reel. In tape, but not machine ready.
   To have a leader and trailer added ($25 charge), use code letter D instead.

2. Inductance is for the primary, measured at 10 kHz, 0.1 Vrms, 0 Adc.
3. Ipk is peak primary current drawn at minimum input voltage.
4. Leakage inductance measured between pins 1 and 3 with pins 4 and 6 shorted.
5. Electrical specifications at 25°C.
Refer to Doc 362 “Soldering Surface Mount Components” before soldering.

Dimensions are in inches [mm]

Recommended Land Pattern

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Specification subject to change without notice.
Please check web site for latest information.