Planar Transformer
For TI UCC2897A Active Clamp PWM Controller

- Developed for Texas Instruments PMP4804 and PMP5997 Demo Board/Reference Design
- Designed as active clamp in forward topology
- Auxiliary winding provides current to the chipset
- Input voltage range: 36 – 75 V

**Core material** Ferrite  
**Terminations** RoHS matte tin over nickel over brass. Other terminations available at additional cost.

**Weight** 11.6 g

**Ambient temperature** -40°C to +85°C

**Storage temperature** Component: -40°C to +85°C. Tape and reel packaging: -40°C to +80°C

**Resistance to soldering heat** Max three 40 second reflows at +260°C, parts cooled to room temperature between cycles

**Moisture Sensitivity Level (MSL)** 1 (unlimited floor life at <30°C / 85% relative humidity)

**Packaging** 200 per 13” reel Plastic tape: 44 mm wide, 0.37 mm thick, 32 mm pocket spacing, 9.35 mm pocket depth

**PCB washing** Tested with pure water or alcohol only. For other solvents, see Doc787_PCB_Washing.pdf

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<table>
<thead>
<tr>
<th>Part number1</th>
<th>Output power (W)</th>
<th>Output voltage nom (V)</th>
<th>Output current (Adc)</th>
<th>Primary inductance2 min (µH)</th>
<th>Leakage inductance max (µH)</th>
<th>DCR max4</th>
<th>Turns ratio pri: sec1: sec2: aux</th>
<th>Pri/sec isolation (Vdc)</th>
</tr>
</thead>
<tbody>
<tr>
<td>JA4309-AL__</td>
<td>100</td>
<td>3.3</td>
<td>30</td>
<td>67.5</td>
<td>0.35</td>
<td></td>
<td>6 : 1 : 1 : 4</td>
<td>1500</td>
</tr>
</tbody>
</table>

1. When ordering, please specify a **packaging** code: 

- **Packaging: D** = 13” machine ready reel. EIA-481 embossed plastic tape (200 per full reel). Quantities less than full reel available: in tape (not machine ready) or with leader and trailer ($25 charge).
- **B** = Less than full reel. In an effort to simplify our part numbering system, Coilcraft is eliminating the need for multiple packaging codes. When ordering, simply change the last letter of your part number from B to D.

2. Inductance measured on an Agilent/HP 4284 between pins 2 and 4 at 200 kHz, 0.1 Vrms, 0 Adc.

3. Leakage inductance measured between pins 2 and 4 at 100 kHz, 0.1 Vrms, 0 Adc with all secondary pins shorted.

4. **DCR for the secondary is for each winding.**

5. **Electrical specifications at 25°C.**

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**Recommended Land Pattern**

- Dot indicates pin 1
- Parts manufactured prior to December 2011 may be marked differently.

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**Dimensions are in** inches / mm

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Coilcraft
www.coilcraft.com

US +1-847-639-6400 sales@coilcraft.com
UK +44-1236-730595 sales@coilcraft-europe.com
Taiwan +886-2-2264 3646 sales@coilcraft.com.tw
China +86-21-6218 8074 sales@coilcraft.com.cn
Singapore +65-6484 8412 sales@coilcraft.com.sg

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