Flyback Transformer
For Texas Instruments
LM5071 PoE PD Controller

- Designed to operate in continuous mode at 250 kHz
- TI App Note AN-1430 specifies 84% efficiency at 3 A.
- 1500 Vrms, one minute isolation from primary and aux to secondary

**Core material** Ferrite

**Terminations** RoHS tin-silver over tin over nickel over phos bronze.
Other terminations available at additional cost.

**Weight** 5.97 g

**Ambient temperature** −40°C to +85°C

**Storage temperature** Component: −40°C to +85°C.
Tape and reel packaging: −40°C to +80°C

**Resistance to soldering heat** Max three 40 second reflows at +260°C, parts cooled to room temperature between cycles

**Moisture Sensitivity Level (MSL)** 1 (unlimited floor life at <30°C / 85% relative humidity)

**Failures in Time (FIT) / Mean Time Between Failures (MTBF)**
38 per billion hours / 26,315,789 hours, calculated per Telcordia SR-332

**Packaging** 175 per 13” reel Plastic tape: 32 mm wide, 0.5 mm thick,
Pcb washing Tested to MIL-STD-202 Method 215 plus an additional aqueous wash. See [Doc787_PCB_Washing.pdf](#).

### Specifications

<table>
<thead>
<tr>
<th>Part number¹</th>
<th>L at 0 A² ±10% (µH)</th>
<th>L at Ipk³ min (µH)</th>
<th>Input voltage (V)</th>
<th>DCR max (Ohms)</th>
<th>Leakage L⁴ max (µH)</th>
<th>Turns ratios</th>
<th>Ipk³ (A)</th>
<th>Outputs⁵</th>
</tr>
</thead>
<tbody>
<tr>
<td>DA2383-AL_</td>
<td>127.0</td>
<td>114.3</td>
<td>24 –60</td>
<td>0.310 (aux)</td>
<td>1.25</td>
<td>2:1 (pri : aux)</td>
<td>1.0</td>
<td>10 V, 0.05 A (aux)</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>0.012 (sec)</td>
<td></td>
<td>6:1 (pri : sec)</td>
<td>3.3 V, 4.0 A (sec)</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>0.255 (pri)</td>
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</tbody>
</table>

1. When ordering, please specify **packaging** code:

   **Packaging:**
   - **D** = 13” machine-ready reel. EIA-481 embossed plastic tape (175 parts per full reel).
   - **B** = Less than full reel. In tape, but not machine ready.
   - To have a leader and trailer added ($25 charge), use code letter D instead.

2. Inductance is for the primary, measured at 200 kHz, 0.2 Vrms, 0 Adc.
3. Ipk is peak primary current drawn at minimum input voltage.
4. Leakage inductance measured on the primary winding with all secondary pins shorted.
5. Output of the secondary is with the windings connected in parallel.
6. Electrical specifications at 25°C.

Refer to Doc 362 "Soldering Surface Mount Components" before soldering.

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**Recommended Land Pattern**

- Dimensions are in inches
- Secondary windings to be connected in parallel on the PC board

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