

- Designed for use with the UCC25800-Q1 from Texas Instruments for isolated gate driver bias supply
- LLC transformers with low interwinding capacitance
- 5656 VDC / 4000 Vrms, one minute isolation (hipot) between primary and secondary
- 7 mm creepage and clearance Material Group 1 (CTI > 600)⁸
- AEC-Q200 qualified

Core material Ferrite

Terminations RoHS tin-silver over tin over nickel over phos bronze.

Weight 2.4 - 2.7 g

Max part temperature +165°C

Ambient temperature -40°C to +125°C

Storage temperature Component: -40°C to +125°C.

Tape and reel packaging: -40°C to +80°C

Resistance to soldering heat Max three 40 second reflows at +260°C, parts cooled to room temperature between cycles

Moisture Sensitivity Level (MSL) 1 (unlimited floor life at <30°C / 85% relative humidity)

Packaging 200/13" reel Plastic tape: 32 mm wide, 0.5 mm thick, 20 mm pocket spacing, 12.3 mm pocket depth

PCB washing Tested with pure water or alcohol only. For other solvents, see Doc787_PCB_Washing.pdf

Pari	L rt at 0 A ²	L DCR max art at 0 A ² (Ohms)	Irms ³ (A)	Leakage ⁴ L max	Turns ratio	Capacitance ⁵	Isolation ⁶	Volt-time ⁷ product
numb	per¹ ±35% (μΗ	nber ¹ ±35% (µH) pri sec p	ri sec	(µH)	pri : sec	max (pF)	(VDC / Vrms)	(V-µsec)
ZE2635-E	BED 100	5-BED 100 0.075 0.153 1.4	56 0.78	2.28	1:2	2.43	5656 / 4000	21
ZE2636-E	BED 100	6-BED 100 0.075 0.097 1.4	43 1.10	2.42	1:1.3	1.78	5656 / 4000	21
ZE2637-E	BED 100	7-BED 100 0.075 0.129 1.5	51 0.89	2.36	1:1.7	2.27	5656 / 4000	21
ZE2638-E	BED 100	B-BED 100 0.075 0.027 1.0	07 2.68	2.60	1:0.40	1.61	5656 / 4000	21
ZE2635-E ZE2636-E ZE2637-E	BED 100 BED 100 BED 100	5-BED 100 0.075 0.153 1.3 6-BED 100 0.075 0.097 1.7 7-BED 100 0.075 0.129 1.3	56 0.78 43 1.10 51 0.89	2.28 2.42 2.36	1 : 2 1 : 1.3 1 : 1.7	2.43 1.78 2.27	5656 / 4000 5656 / 4000 5656 / 4000	21 21 21

- 1. Packaging: D = 13" machine-ready reel. EIA-481 embossed plastic tape (200 parts per full reel). Quantities less than full reel available: in tape (not machine ready) or with leader and trailer (\$25 charge).
- 2. Inductance is for the primary, measured at 333 kHz, 0.1 Vrms, 0 Adc
- 3. Currents that cause a 40°C temperature rise from 25°C ambient when applied to the primary and secondary windings simultaneously.
- 4. Leakage Inductance is for the primary, measured at 333 kHz, 0.1 Vrms with secondary windings shorted together.
- 5. Interwinding capacitance from primary to secondary. It is measured from pin 3 to 7 with pin 2 and 6 open at 333 kHz, 0.1 Vrms.
- 6. 5656 VDC / 4000 Vrms, one minute isolation (hipot) measured between primary and secondary.
- 7. Volt-time product is for the primary, between pins 2 and 3.
- 8. Insulation level may be Functional, Basic, or Reinforced depending on application variables such as working voltage, pollution degree, OVC, and altitude. Please contact Coilcraft for full details.
- 9. Electrical specifications at 25°C.

Refer to Doc 362 "Soldering Surface Mount Components" before soldering.

Designed per IEC 61558-1 to meet the following requirements. Please contact Tech_Support@coilcraft.com for questions about suitability for use in other application conditions.

Reinforced insulation example	Basic insulation example		
Working voltage 350 Vrms	Working voltage 800 Vrms		
Altitude 3000 m	Altitude 3000 m		
Overvoltage category II	Overvoltage category II		
Pollution degree 2	Pollution degree 2		

7 mm creepage and clearance between primary and secondary windings with Material Group I.



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risk applications without prior Coilcraft approval Specification subject to change without notice Please check web site for latest information

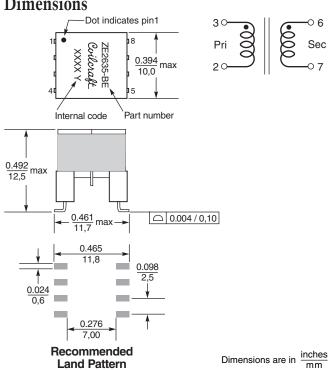




Dimensions









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